IN THE CLAIMS:

Please cancel Claims 1 to 11 without prejudice to or disclaimer of the subject matter recited therein.

Please add new Claims 12 to 17 to read as follows.

Claims 1 to 11 (Cancelled).

12. (New) An image pickup apparatus comprising:

a plurality of sensors, each performing photoelectric conversion and generating a photoelectric conversion signal;

a plurality of amplifiers, each of said plurality of amplifiers corresponding to a respective one of said plurality of sensors, where a signal from each of said plurality of sensors is output through a corresponding one of said plurality of amplifiers;

a plurality of reset switches, each of said plurality of reset switches corresponding to a respective one of said plurality of amplifiers and being connected to an input portion of the corresponding one of the plurality of amplifiers for resetting the input portion;

read-out circuitry operable in a first mode, in which a first signal including a photoelectric conversion signal accumulated in the sensor for a predetermined time period is read out through the corresponding amplifier, and a second mode, in which a second signal including a noise signal obtained from the amplifier by resetting the input portion of the amplifier with the corresponding reset switch is read out; and

correction circuitry for correcting a first signal obtained in the first mode using a second signal obtained in the second mode.

13. (New) An apparatus according to Claim 12, further comprising:

a common output line for sequentially outputting signals output from said plurality of amplifiers, and

scanning circuitry for sequentially reading out the signals output from said plurality of amplifiers to said common output line.

14. (New) An apparatus according to Claim 13, wherein said correction circuitry is connected to said common output line.

15. (New) An apparatus according to Claim 12, wherein said correction circuitry includes subtraction processing circuitry for obtaining a difference between the signal obtained in the first mode and the signal obtained in the second mode.

16. (New) An apparatus according to Claim 15, wherein said correction circuitry includes a clamp circuit.

17. (New) An apparatus according to Claim 12, where said read-out circuitry is a driver which supplies a pulse, said apparatus further comprising:

signal processing circuitry for performing signal processing of a signal output from said correction circuitry; and

control circuitry for controlling said signal processing circuitry.